Scope

The 12th HIPS workshop is a full-day meeting to be held at the IPDPS 2007 focusing on high-level programming of chip multi-processors (multi-core PCs), computing clusters, and massively-parallel machines. Like its predecessors, the workshop seeks cross-fertilizing research in areas of parallel applications, language design, compilers, run-time systems, and programming tools. It provides a timely and lightweight forum for scientists and engineers to present the latest ideas, findings, and tools in these rapidly changing fields. This year we especially encourage innovative approaches for programming the increasingly popular chip multi-processors and the fast growing large-scale parallel systems. The topics include but are not limited to

- new programming constructs for exploiting parallelism and locality
- experience with and improvements for existing parallel languages and run-time environments such as MPI, OpenMP, HPF, Cilk, UPC, and Co-array Fortran.
- parallel programming tools and environments
- (scalable) performance analysis, modeling, and monitoring
- OS and architectural support for parallel programming and debugging
- software and system support for extreme scalability including the issues of fault tolerance

Important Dates

Papers due: December 22nd, 2006
Author notification: January 12th, 2007
Camera-ready final papers due: January 22nd, 2007 (strict deadline set by the conference)

The HIPS workshop proceedings will be published electronically along with the IPDPS conference proceedings.

Workshop Chair
Chen Ding University of Rochester, USA

Steering Committee
Rudolf Eigenmann Purdue University, USA
Michael Gerndt Technische Uni. München, Germany
Frank Müller North Carolina State Uni., USA
Craig Rasmussen Los Alamos Natl. Lab, USA
Martin Schulz Lawrence Livermore Natl. Lab, USA

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Program Committee
Arun Chauhan Indiana University, USA.
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